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International Patent Application of

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UMAYABASHI, M.

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For: DELAY-COMPENSATED TIMESLOT ASSIGNMENT METHOD AND SYSTEM ...

Hon. Commissioner of Patents and Trademarks
Washington, DC 20231
Attention: Application Branch

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SUBMISSION

Sir:

<input checked="" type="checkbox"/>	Submitted herewith is a copy of art together with an ART LISTING FORM listing the same for the convenience of the Examiner.
<input checked="" type="checkbox"/>	FORMAL DRAWINGS - Sheet 1 to 10, Figures 1 to 13.

EXPRESS MAIL CERTIFICATE

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail Post Office to Addressee (mail label #EL613109475US) in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231-9999, on February 26, 2001:

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February 26, 2001

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Respectfully submitted,

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